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AND UNFAIR COMPETITION LAW
AND RELATED LITIGATION

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November 21, 2005

FACSIMILE COVER SHEET

To: Examiner Phat X. Cao
Group Unit No. 2814
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Fax: 571-273-8300

From: William R. Allen, Ph.D.

Re: US Application Serial No. 10/767,039
Filed: January 29, 2004
Title: VERTICAL FIELD EFFECT
TRANSISTORS INCORPORATING
SEMICONDUCTING NANOTUBES
GROWN IN A SPACER-DEFINED
PASSAGE
Applicants: Toshiharu Furukawa et al.
Atty Docket: ROC920030272US1
(IBM-273)

Pages: 15 (including cover sheet)

MESSAGE/COMMENTS

Please see the attached Amendment Under 37 C.F.R. §1.111 for
filing in connection with the above-referenced application.

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William R. Allen 21 November 2005
William R. Allen Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Furukawa et al. Art Unit: 2814
Serial No.: 10/767,039 Examiner: Phat X. Cao
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SEMICONDUCTING NANOTUBES GROWN IN A SPACER-DEFINED
PASSAGE

AMENDMENT UNDER 37 C.F.R. § 1.111

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated August 23, 2005, please amend the above-identified application as follows:

Amendments to the Specification are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 8 of this paper.

Page 1 of 14
Serial No. 10/767,039
Amendment and Response dated November 21, 2005
Reply to Office Action of August 23, 2005
IBM Docket ROC920030272US1
WH&E IBM/273
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